

INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Not for submission under 37 CFR 1.99)	Application Number		09989777
	Filing Date		2001-11-19
	First Named Inventor	Craig Nemecek	
	Art Unit	2123	
	Examiner Name	Mary C. Jacob	
	Attorney Docket Number	CD01208M	

U.S.PATENTS						
Examiner Initial*	Cite No	Patent Number	Kind Code:	Issue Date	Name of Patentee or Applicant of cited Document	Pages,Columns,Lines where Relevant Passages or Relevant Figures Appear
	1	7042301	B1	2006-05-09	Sutardja, Sehat	Entire Document
	2	5594388	B1	1997-01-14	O'Shaughnessy et al.	Entire Document
	3	5963105	B1	1999-10-05	Nguyen	Entire Document
	4	5982241	B1	1999-11-09	Nguyen et al.	Entire Document
	5	6133773	B1	2000-10-17	Garlepp et al.	Entire Document
	6	6956419	B1	2005-10-18	Mann et al.	Entire Document
	7	7139530	B1	2006-11-21	Kusbel	Entire Document
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9	7129793	B1	2006-10-31	Gramegna	Entire Document
10	7250825	B1	2007-07-31	Wilson et al.	Entire Document
11	7200507	B1	2007-04-03	Chen et al.	Entire Document
12	7421251	B1	2008-09-02	Westwick et al.	Entire Document
13	7542533	B1	2009-06-02	Jasa et al.	Entire Document
14	7154294	B1	2006-12-26	Liu et al.	Entire Document
15	6809566	B1	2004-10-26	Xin-LeBlanc, Jane	Entire Document
16	6154064	B1	2000-11-28	Proebsting, Robert J.	Entire Document
17	6037807	B1	2000-03-14	Wu et al.	Entire Document
18	4326135	B1	1982-04-20	Jarrett et al.	Entire Document
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21	5432476	B1	1995-07-11	Tran	Entire Document
22	5929710	B1	1999-07-27	Bien	Entire Document
23	5945878	B1	1999-08-31	Westwick et al.	Entire Document
24	6566961	B1	2003-05-20	Dasgupta et al.	Entire Document
25	6717474	B1	2004-04-06	Chen et al.	Entire Document
26	6806771	B1	2004-10-19	Hilderbrant et al.	Entire Document
27	6819142	B1	2004-11-16	Viehmann et al.	Entire Document
28	6836169	B1	2004-12-28	Richmond et al.	Entire Document
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33	7119602	B1	2006-10-10	Davis	Entire Document
34	7265633	B1	2007-09-04	Stiff, Jonathon	Entire Document
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36	6466078	B1	2002-10-15	Stiff, Jonathon	Entire Document
37	5896068	B1	1999-04-20	Moyal	Entire Document
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42	5952888	B1	1999-09-14	Scott	Entire Document
43	6026134	B1	2000-02-15	Duffy et al.	Entire Document
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46	6275117	B1	2001-08-14	Abugharbieh et al.	Entire Document
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48	6310521	B1	2001-10-30	Dalmia	Entire Document
49	6377646	B1	2002-04-23	Sha	Entire Document
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FOREIGN PATENT DOCUMENTS

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	1	Application Number 11/850,260 (CD2007006): "Circuit and Method for Improving the Accuracy of a Crystal-less Oscillator Having Dual-Frequency Modes," Wright et al.; 33 pages 9/5/07	<input type="checkbox"/>
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	4	USPTO Advisory Action for Application Number 11/644,100 (CD05204) dated 02/09/2009; 3 pages	<input type="checkbox"/>
	5	USPTO Final Rejection for Application Number 11/644,100 (CD05204) dated 11/18/2008; 12 pages	<input type="checkbox"/>

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6	USPTO Non-Final Rejection for Application Number 11/644,100 (CD05204) dated 04/14/2008; 10 pages	<input type="checkbox"/>
7	USPTO Notice of Allowance for Application Number 11/415,588 (CD05028) dated 03/11/2008; 6 pages	<input type="checkbox"/>
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9	USPTO Final Rejection for Application Number 11/415,588 (CD05028) dated 10/19/2007; 8 pages	<input type="checkbox"/>
10	USPTO Non-Final Rejection for Application Number 11/415,588 (CD05028) dated 06/13/2007; 8 pages	<input type="checkbox"/>
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12	USPTO Non-Final Rejection for Application Number 12/218,404 (CD05028C1) dated 09/30/2008; 8 pages	<input type="checkbox"/>
13	Application Number 11/644,100 (CD05204): "Differential-to-single ended signal converter circuit and method," Stiff; 33 pages 12/21/06	<input type="checkbox"/>
14	Application Number 11/415,588 (CD05028): "Voltage Controlled Oscillator Delay Cell and Method," Sivadasan; 24 pages 5/1/06	<input type="checkbox"/>
15	Application Number 12/218,404 (CD05028C1): "Voltage Controlled Oscillator Delay Cell and Method," Sivadasan; 23 pages 7/14/08	<input type="checkbox"/>
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17	USPTO Non-Final Rejection for Application Number 11/132,894 (CD04064) dated 12/19/2006; 12 pages	<input type="checkbox"/>
18	Application Number 11/132,894 (CD04064): "Open Loop Bandwidth Test Architecture and Method for Phase Locked Loop (PLL)," Stiff; 38 pages 5/19/05	<input type="checkbox"/>
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25	Application Number 11/322,044 (CD05198): "Split charge pump PLL architecture," Stiff; 19 pages 12/28/05	<input type="checkbox"/>
26	USPTO Notice of Allowance for Application Number 10/305,589 (CD02181) dated 02/04/2005; 5 pages	<input type="checkbox"/>
27	USPTO Final Rejection for Application Number 10/305,589 (CD02181) dated 10/21/2004; 7 pages	<input type="checkbox"/>

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28	USPTO Non-Final Rejection for Application Number 10/305,589 (CD02181) dated 10/07/2003; 6 pages	<input type="checkbox"/>
29	Application Number 10/305,589 (CD02181): "Current Controlled Delay Circuit," Stiff; 18 pages 11/26/02	<input type="checkbox"/>
30	Application Number 09/849,164 (CD01035): "Reduced Static Phase Error CMOS PLL Charge Pump," Stiff; 30 pages 5/4/01	<input type="checkbox"/>
31	Maneatis, "Low-Jitter Process-Independent DLL and PLL Based on Self-Biased Techniques," IEEE Journal of Solid-State Circuits, Vol. 31, No. 11, November 1996; 10 pages	<input type="checkbox"/>
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33	USPTO Notice of Allowance for Application Number 10/327,217 (CD02174) dated 08/12/2004; 6 pages	<input type="checkbox"/>
34	USPTO Non-Final Rejection for Application Number 10/327,217 (CD02174) dated 04/30/2004; 5 pages	<input type="checkbox"/>
35	USPTO Miscellaneous Action for Application Number 10/327,217 (CD02174) dated 02/10/2004; 1 page	<input type="checkbox"/>
36	Application Number 10/327,217 (CD02174): "Single Ended Clock Signal Generator Having a Differential Output," Richmond et al.; 27 pages 12/20/02	<input type="checkbox"/>
37	USPTO Notice of Allowance for Application Number 10/871,582 (CD03036) dated 03/30/2006; 6 pages	<input type="checkbox"/>
38	USPTO Final Rejection for Application Number 10/871,582 (CD03036) dated 02/01/2006; 5 pages	<input type="checkbox"/>

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39	USPTO Non-Final Rejection for Application Number 10/871,582 (CD03036) dated 09/07/2005; 7 pages	<input type="checkbox"/>
40	Application Number 10/871,582 (CD03036): "LVDS Input Circuit with Extended Common Mode Range," Reinschmidt et al.; 25 pages 6/17/04	<input type="checkbox"/>
41	USPTO Notice of Allowance for Application Number 09/404,891 (CD99054) dated 03/04/2005; 6 pages	<input type="checkbox"/>
42	USPTO Final Rejection for Application Number 09/404,891 (CD99054) dated 12/08/2004; 7 pages	<input type="checkbox"/>
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47	USPTO Non-Final Rejection for Application Number 09/404,891 (CD99054) dated 10/11/2002; 5 pages	<input type="checkbox"/>
48	Application Number 09/404,891 (CD99054): "Method, Architecture and Circuitry for Controlling Pulse Width in a Phase and/or Frequency Detector," Scott et al.; 17 pages 9/24/99	<input type="checkbox"/>
49	USPTO Notice of Allowance for Application Number 10/226,911 (CD02112) dated 08/20/2004; 4 pages	<input type="checkbox"/>

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Examiner Signature	/Mary Jacob/	Date Considered	07/29/2009
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